

# PROGRAM AT A GLANCE



<b>Tuesday, July 2nd</b>	
<b><i>Amphitheater 1</i></b>	
11:00-12:30	<b>Tutorial – Machine Learning for Design Automation</b>
12:30-13:30	<b>Lunch</b>
13:30-15:00	<b>Tutorial – Exploring Radiation Effects for Reconfigurable SoCs in Space and Particle Accelerators</b>
15:00-15:30	<b>Coffee Break</b>
15:00-17:00	<b>Tutorial – Integrating Multiple Knowledge-based Automation Methodologies into the A/MS IC Design Flow</b>
20:30-22:00	<b>Welcome Reception</b>

Wednesday, July 3rd		
	<i>Amphitheater 1</i>	<i>Amphitheater 2</i>
08:30-09:00	Opening Session	
09:00-10:00	Plenary Talk – <i>Prof. Yusuf Leblebici</i> “Designing Some of the Most Influential Hardware in Human History: Our Collective Responsibilities”	
10:00-10:30	Coffee Break	
10:30-12:10	Automated Circuit Design	<i>Special Session</i> Advances & Future Trends in Electronic Circuits & Systems with Emerging Devices
12:10-13:20	Lunch	
13:20-13:50	CEDA Presentation	
13:50-15:30	EDA Competition I	Cutting-edge Technologies for Automotive and Space
15:30-16:00	Coffee Break	
16:00-17:00	Innovations in Hardware Security I	Machine Learning for Modeling

Thursday, July 4th		
	<i>Amphitheater 1</i>	<i>Amphitheater 2</i>
08:30-09:30	<b>Plenary Talk – Prof. Georges Gielen “Designing Analog Integrated Circuits: Designers, Synthesis or Generative AI?”</b>	
09:30-10:30	<b>EDA Competition II</b>	
10:30-11:00	<b>Coffee Break</b>	
11:00-12:00	<b>Sponsor Presentations</b>	
12:00-13:40	<b>Lunch and Job Fair</b>	
13:40-15:00	<b>Automating Layout and Technology Migration</b>	<b>Advanced Circuit Design I</b>
15:00-15:30	<b>Coffee Break</b>	
15:30-16:30	<b>EDA and the Human Factor</b>	<b><i>Special Session</i> Safety and Security in Modern System-on-Chip (SoC)</b>
20:30-22:00	<b>Gala Dinner</b>	

		Friday, July 5th	
		<i>Amphitheater 1</i>	<i>Amphitheater 2</i>
09:30-10:30		<b>Novel Device Modeling and Characterization</b>	<b>Advanced Circuit Design II</b>
10:30-11:00		<b>Coffee Break</b>	
11:00-12:40		<b>Machine Learning for Design</b>	<b>IC Design Contest</b>
12:40-13:50		<b>Lunch</b>	
13:50-14:50		<b>Advanced Circuit Modeling</b>	<b>Innovations in Hardware Security II</b>
14:50-15:20		<b>Coffee Break</b>	
15:20-16:20		<b>Dealing with Reliability</b>	<b>Dealing with Verification</b>
16:20-16:50		<b>Award Ceremony and Closing</b>	