www.smacd-conference.org info@smacd-conference.org

# Call for EDAGOA AWARD and IAM de HALL OF FAME

# COMPETE for the \$1000 AWARD and JOIN the <u>HALL-OF-FAME</u>

As usual in **SMACD Conferences**, the **2024 edition**, which will take place at Volos, Greece, from the 2<sup>nd</sup> to the 5<sup>th</sup> of July, will also hold a thrilling and dynamic event where M.Sc. and Ph.D. students can compete with their best ideas, methodologies, flows, and tools with one unique yet challenging goal:

## **"ENHANCE DESIGN AUTOMATION FOR INTEGRATED CIRCUITS AND SYSTEMS"**

### HOW TO ENTER THE COMPETITION?

All participants must submit a paper (following the same SMACD 2024 submission guidelines) with a description of the methodology and/or tool, the design problem of their choice, and the solution provided by their methodology and/or tool.

The first author of the submission and presenter at the conference must be a M.Sc. or a Ph.D. student!

### EVALUATION CRITERIA

A Judging Committee will select the winner(s) based on the quality of the paper, of its presentation at the conference and the live demonstration, using the following criteria:

- Complexity of the problem posed in the paper
- Level of automation (e.g., fully automated, user-assisted, etc.)
- Designer interface (e.g., problem setup, result analysis, etc.)
- Applicability of the proposal (e.g., general approach or restricted to special problems, circuit classes or sizes, etc.)
- **Robustness** of the design solutions (e.g., against parasitics, variability, etc.) and of the proposal itself (e.g., convergence, insensitivity to input and/or starting values, etc.)
- Integration degree with commercial design suites and design methodologies (e.g., import and export data from these suites, usability in one or more steps of current design flows, etc.)

### TOPICS OF INTEREST

Methodologies and tools related to all topics of the SMACD24 conference (e.g., Synthesis, Sizing, Optimization, Modeling and Simulation of AMS, RF and multidomain circuits, ML and Cloud Computing EDA methodologies, EDA methodologies for Hardware Security and Emerging Devices, etc.).

### ORGANIZED BY





Department of Electrical and Computer Engineering

University of Thessaly

### IMPORTANT NOTES

- All accepted submissions in the competition must be presented by the students in special sessions at the conference and published in the conference proceedings.
- The presenters **must perform a live demo** of the methodologies and/or tool.
- SMACD organization guarantees internet connection to all participants, but required CAD tools or design kits licensing is in responsibility of each participant.
- Papers not accepted for the competition short-list event will still be considered for the SMACD 2024 regular sessions.

### EDA COMPETITION CHAIRS

Elisenda Roca, IMSE-CNM, USeville and CSIC, ES Vasilis Pavlidis, Aristotle Univ. of Thessaloniki, GR Konstantis Daloukas, ANSYS, GR

### DATES

Paper Submission Deadline ..... March 10<sup>th</sup>, 2024 Author Notification ...... March 26<sup>th</sup>, 2024 Camera Ready Paper Deadline ..... April 8<sup>th</sup>, 2024 Early Registration Deadline ...... April 8<sup>th</sup>, 2024

### CONFERENCE VENUE

Department of Electrical & Computer Engineering University of Thessaly

Sekeri & Cheiden st., Pedion Areos Volos, Greece

