

SMACD24

Call for IC DESIGN CONTEST

COMPETE for the \$1000 AWARD and be on the HALL-OF-FAME

The **2024 edition** of the **SMACD Conference**, which will take place at Volos, Greece, from the 2nd to the 5th of July, will also hold a thrilling and dynamic event where M.Sc. and Ph.D. students can compete showing their best use of EDA methodologies to reach one unique yet challenging goal:

“ENHANCE INTEGRATED CIRCUITS AND SYSTEMS DESIGN”

HOW TO ENTER THE COMPETITION?

All participants must submit a paper (following **SMACD 2024 submission guidelines**) that fits into one or more of the following categories:

- (1) Novel IC design whose performance competes within the state-of-the-art of its research field (at least including Post-Layout simulations)
- (2) IC design where at least one step of its development flow was partially (or fully) assisted by custom/academic Computer-Aided Design and/or Electronic Design Automation tools
- (3) IC design clearly identifying one or more steps of the development flow that would greatly benefit from the development of new Computer-Aided Design and/or Electronic Design Automation tools

The layout of the circuit must be present and a prototype test is considered an added value. **The first author of the submission and presenter at the conference must be a M.Sc. or a Ph.D. student!**

EVALUATION CRITERIA

A Judging Committee will select the winner(s) based on the quality of the paper, the presentation at the conference, and the Q&A during the special session, using the following criteria:

- (1) **Complexity** of the problem posed in the paper
- (2) **Level of CAD/EDA support** to reach the presented IC design
- (3) **Robustness** of the design solution (e.g., against parasitics, variability, reliability, etc.)

TOPICS OF INTEREST

IC designs resulting or enhanced by EDA methodologies and tools related to all application topics of the SMACD24 (e.g., AMS, RF and multidomain circuits, and, also, emerging devices).

ORGANIZED BY



Department of Electrical and Computer Engineering
University of Thessaly

IMPORTANT NOTES

- All accepted submissions in the IC Design Contest must be **presented by the students in special sessions at the conference** and **published in the conference proceedings**.
- Papers not accepted for the competition short-list event will still be considered by the technical program committee for the **SMACD 2024 regular sessions**.

IC DESIGN CONTEST CHAIRS

Kostas Adam, Synopsys, USA
Georgios Panagopoulos, Intel & NTU Athens, GR
Dimitrios Garyfallou, University of Thessaly, GR

DATES

Paper Submission Deadline **March 10th, 2024**
Author Notification **March 26th, 2024**
Camera Ready Paper Deadline **April 8th, 2024**
Early Registration Deadline **April 8th, 2024**

CONFERENCE VENUE

Department of Electrical & Computer Engineering
University of Thessaly

Sekeri & Cheiden st., Pedion Areos
Volos, Greece

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