

also hold an attractive event where M.Sc. and Ph.D. students can compete showing novel IC designs to reach one unique yet challenging goal:

"DESIGN HIGH PERFORMING INTEGRATED CIRCUITS AND SYSTEMS"

HOW TO ENTER THE CONTEST?

All participants must submit a paper (following SMACD 2025 submission guidelines) that fits into one or more of the following categories:

- **Novel IC design** whose performance competes within the **state-of- the- art** of its research field (at least including Post-Layout simulations).
- IC design where at least one step of its development flow was partially (or fully) assisted by custom/academic **CAD and/or EDA** tools.
- IC design clearly identifying one or more steps of the development flow that would greatly benefit from the development of new CAD and/or EDA tools
- The layout of the circuit must be present and a prototype test is considered an added value. The first author of the submission and presenter at the conference must be a M.Sc. or a Ph.D. student!

IMPORTANT DETAILS

- All accepted submissions in the IC Design Contest must be presented by the students in special sessions at the conference and published in the conference proceedings.
- Papers not accepted for the competition short-list event will still be considered by the technical program committee for the SMACD 2025 regular sessions.

TOPICS OF INTEREST

IC designs resulting or enhanced by EDA methodologies and tools related to all application topics of the SMACD 2025 (e.g., AMS, RF and multidomain circuits, and, also, emerging devices).

IMPORTANT DATES

Paper Submission DeadlineFebruary 14, 2025Author NotificationApril 8, 2025Camera Ready Paper SubmissionApril 30, 2025Early Registration DeadlineApril 30, 2025

CONTACT INFO

info@smacd-conference.org

www.smacd-conference.org

EVALUATION CRITERIA

A Judging Committee will select the winner(s) based on the quality of the paper, the presentation at the conference, and the Q&A during the special session, using the following criteria:

- The performance of the design compared to the state of the art.
- Complexity of the problem posed in the paper.
- Level of CAD/EDA usage.
- Robustness of the design solution (e.g., against parasitics, variability, reliability, etc.)

IC DESIGN CONTEST CHAIRS

- Burcu Erkmen, Yıldız Technical University, TR
- İhsan Çiçek, Gebze Technical Univeristy, TR

CONFERENCE VENUE

Istanbul Bilgi University, Santral Istanbul Kampus Emniyettepe, Kazım Karabekir Cd. No: 2/13, 34060 Eyüpsultan/ İstanbul



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