

of July, will also hold a thrilling and dynamic event where B.Sc/M.Sc/Ph.D. students compete with their best ideas, methodologies, flows, and tools with one unique yet challenging goal:

"EMPOWER THE FUTURE OF DESIGN AUTOMATION

FOR INTEGRATED CIRCUITS AND SYSTEMS"

HOW TO APPLY?

- All participants must submit a paper via EasyChair under the 'EDA Competition' tab, following the same SMACD 2025 submission guidelines.
- The submitted paper must present an EDA solution!
- The first author of the submission and presenter at the conference must be a student (B.Sc., M.Sc. or a Ph.D.)

IMPORTANT DETAILS

- All accepted submissions in the competition must be presented by the students in special sessions at the conference and will be published in the conference proceedings.
- The presenters must perform a live demo of the methodologies and/or tool.
- SMACD organization guarantees internet connection to all participants, but required CAD tools or design kits licensing is in responsibility of each participant.
- Papers not accepted for the competition short-list event may still be considered for the SMACD 2024 regular sessions.

TOPICS OF INTEREST

Methodologies and tools related to all topics of the SMACD25 conference (e.g., Synthesis, Sizing, Optimization, Modeling and Simulation of AMS, RF and multidomain circuits, ML and Cloud Computing EDA methodologies, EDA methodologies for Hardware Security and Emerging Devices, etc.).

IMPORTANT DATES

Paper Submission Deadline......February 14, 2025
Author Notification.....April 8, 2025
Camera Ready Paper Submission......April 30, 2025

Early Registration Deadline......April 30, 2025

ORGANIZED BY





EVALUATION CRITERIA

A Judging Committee will select the winner(s) based on the quality of the paper, of its presentation at the conference and the live demonstration, regarding the following criteria:

- Complexity of the problem posed in the paper
- Level of automation (e.g., fully automated, user-assisted, etc.) Designer interface (e.g., problem setup, result analysis, etc.)
 Applicability of the proposal (e.g., general approach or restricted to special problems, circuit classes or sizes, etc.)
- Robustness of the design solutions (e.g., against parasitics, variability, etc.) and of the proposal itself (e.g., convergence, insensitivity to input and/or starting values, etc.)
- Integration degree with commercial design suites and design methodologies (e.g., import and export data from these suites, usability in one or more steps of current design flows, etc.)

EDA COMPETITION CHAIRS

- Revna Vural Acar, Yıldız Technical University, TR
- · Sarah Azimi, Politecnico di Torino, IT

CONFERENCE VENUE

Istanbul Bilgi University, Santral Istanbul Kampus Emniyettepe, Kazım Karabekir Cd. No: 2/13, 34060 Eyüpsultan/ İstanbul



CONTACT INFO

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