



Call for Special Session on

Novel Circuit Techniques and Design Methodologies for Accuracy Improvement of DACs and ADCs



This session focuses on recent advances in the design of high-accuracy and robust DACs and ADCs for diverse applications, including healthcare, quantum computing, and harsh environments. It covers novel architectures, advanced calibration techniques, optimized methodologies, and AI-powered enhancements, addressing challenges in resolution, linearity, and reliability. The session aligns with the SMACD conference's modeling and design focus, appealing to both academic and industrial audiences across fields like analog design, mixed-signal systems, and VLSI.

Keywords: DAC, DAC, calibration, mismatch, assisted design

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Submit a Paper

Important Dates

- Paper Submission Deadline.....March 14, 2025**
- Author Notification.....April 22, 2025
- Camera Ready Paper Submission.....May 6, 2025
- Early Registration Deadline.....May 6, 2025

**7-10
July
2025**

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