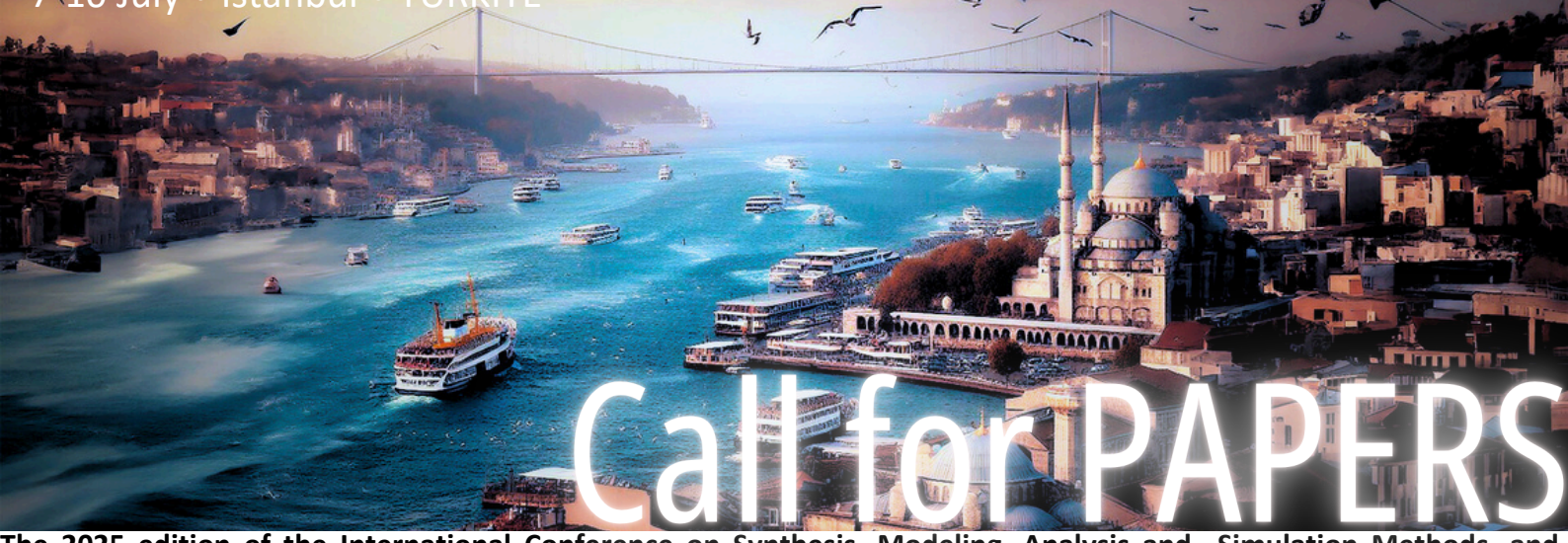


# SMACD 2025

where circuits come alive

7-10 July • İstanbul • TÜRKİYE

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# Call for PAPERS

The 2025 edition of the International Conference on Synthesis, Modeling, Analysis and Simulation Methods, and Applications to Circuit Design (SMACD) will be held from 7 to 10 July in İstanbul, TÜRKİYE. SMACD is a forum devoted to modeling, simulation, and synthesis for Analog, Mixed-signal, RF (AMS/RF), and multi-domain (nanoelectronics, biological, MEMS, optoelectronics, etc.) integrated circuits and systems, as well as, emerging technologies and applications. Open-source tools and methods for IC design and experiences with modeling, simulation, and synthesis techniques in diverse application areas are also welcomed. Objective technologies include CMOS, beyond CMOS, and More-than-Moore such as MEMs, power devices, sensors, passives, etc. The Conference Proceedings will be published at the IEEE Xplore® Digital Library.

## CAD AND EDA METHODS AND TOOLS FOR AMS SYSTEMS

- | Synthesis, Sizing & Optimization   | Modeling  | Simulation, Verification & Test  |
|--|---|--|
| <ul style="list-style-type: none"> <li>Multi-level Synthesis Methods</li> <li>Physical Synthesis Methods</li> <li>High-frequency Circuits &amp; Systems</li> <li>Low-Power &amp; Energy-Aware Design</li> <li>Parasitic-Aware Design</li> <li>Variability-aware &amp; Reliability-Aware</li> <li>Sizing &amp; Optimization Methods</li> <li>Procedural Design Methods</li> </ul> | <ul style="list-style-type: none"> <li>Performance/Behavioral</li> <li>Power and Electro-therma</li> <li>Reliability and Variability</li> <li>RF/microwave/mm-wave</li> <li>Signal/Power Integrity</li> <li>Electromagnetic Theory</li> <li>Transmission Line Theory</li> <li>Automated Model Gen.</li> </ul> | <ul style="list-style-type: none"> <li>Behavioral Simulation</li> <li>Numerical &amp; Symbolic</li> <li>RF Circuit Simulation</li> <li>Electromagnetic Simulation</li> <li>Multilevel Simulation</li> <li>Analysis of Variability Effects</li> <li>Formal/Functional Verification</li> <li>Test &amp; Design-for-Test</li> </ul> |

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## EMERGING TECHNOLOGIES AND APPLICATIONS (ETA)

- | CAD for Emerging Technologies   | ML for CAD/EDA  | Hardware Security  |
|---|---|--|
| <ul style="list-style-type: none"> <li>CAD for Bio-Electronic Devices, Bio-Sensors</li> <li>CAD for Multi-Domain Devices and Circuits</li> <li>CAD using Cloud Computing</li> <li>AMS Soft and Hard IP Blocks Generating Meth.</li> </ul> | <ul style="list-style-type: none"> <li>ML for device/circuit modelling</li> <li>ML for circuit/system simulation</li> <li>ML for automatic sizing and layout</li> </ul> | <ul style="list-style-type: none"> <li>Hardware Security primitives (PUFs, etc.)</li> <li>Attacks and Countermeasures</li> <li>Anticounterfeiting</li> <li>Methods, Architectures and Tools for Secure Design</li> </ul> |

## EMERGING DEVICES AND PARADIGMS

- Emerging Device Modeling
- Design Strategies using Emerging Devices
- Emerging Devices in Security
- Devices and Methods for Bio-Inspired and Neuromorphic Computing

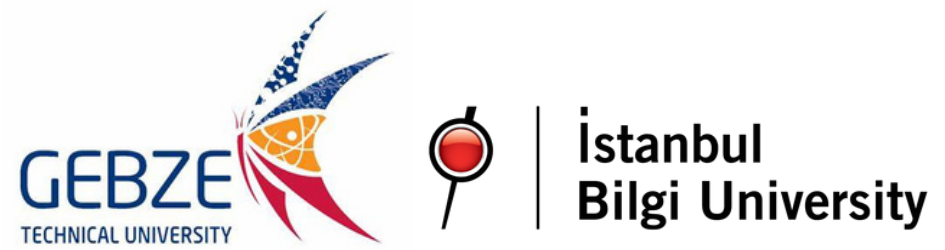
## AMS ICS AND MULTI-DOMAIN DESIGN APPLICATIONS

- Internet of Everything
- Automotive Systems
- Biomedical and Bio-inspired CAS
- Low-Power Low Voltage CAS
- Sensors and Sensing Systems
- Security Systems
- Aerospace Systems
- Renewable Energy Systems

## IMPORTANT DATES

- Paper Submission Deadline.....March 14, 2025**
- Author Notification.....April 22, 2025
- Camera Ready Paper Submission.....May 6, 2025
- Early Registration Deadline.....May 6, 2025

## ORGANIZED BY



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